

**WHAT IS CLAIMED:**

*Sub A* → 1. In a television receiver having a line scanned video display, a method for reducing the visual effects of an artifact in a line scan portion of the video signal display, the artifact being attributable to a periodic signal within the video pass band coupled to a video processing path of a video circuit, the line scan having a frequency of  $f_h$ , comprising:

selecting the frequency of the periodic signal, and  
predetermining the frequency of the periodic signal to be an odd harmonic of  $f_h/2$ .

10 2. The method of claim 1 wherein the periodic signal is a clock signal electrostatically/capacitively coupled to the video circuit.

3. The method of claim 2 wherein the electrostatically/capacitively coupled clock signal is an FM modulating signal of a spread spectrum clock.

15 4. The method of claim 2 wherein the electrostatically/capacitively coupled clock signal is a carrier signal of a spread spectrum clock.

5. The method of claim 1 wherein  $f_h$  is the NTSC standard horizontal scan frequency of 15,734.26573 Hz and the predetermined fundamental frequency of the periodic signal is approximately 39.336 kHz (2.5 multiplied by  $f_h$ ).

20 6. The method of claim 5 wherein the predetermined fundamental frequency of the periodic signal is rounded up or rounded down to an integral number.

7. The method of claim 1 wherein the predetermined fundamental frequency of the periodic signal is one of rounded up and rounded down to an integral number.

25 8. The method of claim 2 wherein the video circuit, and the electrostatically/capacitively coupled periodic signal are included within an integrated circuit having an underlying substrate of semiconductor material.

9. The method of claim 8 wherein the electrostatically/capacitively coupling is via respective capacitances coupled to the underlying substrate.

*Sub A2* → 30 10. The method of claim 1 wherein the periodic signal is electrostatically/capacitively coupled to the video circuit.

11. The method of claim 10 wherein the video circuit, and the electrostatically/capacitively coupled periodic signal are included within a

monolithic integrated circuit having an underlying substrate of semiconductor material.

12. The method of claim 11 wherein the electrostatic coupling is via capacitances to one of the underlying substrate and between component parts of the monolithic integrated circuit.

13. In a television receiver having a line scanned video display, apparatus for reducing the visual effects of an artifact in a line scan portion of the video signal display, the artifact being attributable to a periodic signal within the video passband coupled to a video processing path of a video circuit, the line scan having a frequency of  $f_h$ , comprising:

means for selecting the frequency of the periodic signal, and

means for predetermining the frequency of the periodic signal to be an odd harmonic of  $f_h/2$ .

14. The apparatus of claim 13 wherein the periodic signal is a clock signal electrostatically/capacitively coupled to the video circuit.

15. The apparatus of claim 14 wherein the electrostatically/capacitively coupled clock signal is an FM modulating signal of a spread spectrum clock.

16. The apparatus of claim 14 wherein the electrostatically/capacitively coupled clock signal is a carrier signal of a spread spectrum clock.

17. The apparatus of claim 13 wherein  $f_h$  is the NTSC standard horizontal scan frequency of 15,734.26573 Hz and the predetermined fundamental frequency of the periodic signal is approximately 39.336 kHz (2.5 multiplied by  $f_h$ ).

18. The apparatus of claim 17 wherein the predetermined fundamental frequency of the periodic signal is one of rounded up and rounded down to an integral number.

19. The apparatus of claim 13 wherein the predetermined fundamental frequency of the periodic signal is rounded up or rounded down to an integral number.

20. The apparatus of claim 14 wherein the video circuit, and the electrostatically/capacitively coupled periodic signal are included within an integrated circuit having an underlying substrate of semiconductor material.

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contd

21. The apparatus of claim 20 wherein the electrostatically/capacitively coupling is via respective capacitances coupled to the underlying substrate.

22. The apparatus of claim 13 wherein the periodic signal is  
5 electrostatically/capacitively coupled to the video circuit.

23. The apparatus of claim 22 wherein the video circuit, and the electrostatically/capacitively coupled periodic signal are included within a monolithic integrated circuit having an underlying substrate of semiconductor material.

10 24. The apparatus of claim 23 wherein the electrostatically/capacitively coupling is via capacitances to one of the underlying substrate and directly between component parts of the monolithic integrated circuit.

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